

ABSTRACT OF THE DISCLOSURE

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The data transmission and reception system comprises a data transmitter and a data receiver. The data transmitter generates and transmits a high-speed serial signal through a transmission path and the data receiver receives the serial signal. The data transmitter, when forming the frame for every tributary signal, inserts into the frame a frame bit indicating a boundary of the frame and, after having formed the frame, performs only a bit synchronization with respect to every tributary signal. On the other hand, the data receiver, for a respective tributary signal, stores a data indicated by the tributary signal and, in a timing based on a detection of the frame bit of the tributary signal and a reference frame pulse commonly issued between tributary signals, outputs the stored data to thereby perform the tributary synchronization.